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Docket No.: BAKRAN Serial No.: 10/696,008

AMENDMENTS TO THE CLAIMS WITH MARKINGS TO SHOW CHANGES MADE, AND LISTING OF ALL CLAIMS WITH PROPER IDENTIFIERS

1. (Currently amended) An n-point-converter circuit, comprising:

a maximum of two converter valves for each converter phase electrically

connected in series at corresponding valve connection points, each

converter valve having a maximum of (n-1) turn-off semiconductor switches;

a voltage intermediate circuit having a maximum of (n-1) capacitors

electrically connected in series at corresponding capacitor connection

points, each capacitor connection point defining a corresponding DC

potential; and

a maximum of (n-2) cross arms, each cross arm having a maximum of (n-1)

semiconductor switches, of which at least two are anti-serially connected,

wherein free ends of the series-connected converter valves form DC-side

terminals, said DC-side terminals connected electrically in parallel with the

voltage intermediate circuit, and

wherein the (n-2) cross arms connect the valve connection point of each

series-connected pair of the converter valves with a corresponding one of

the intermediate potentials of the voltage intermediate circuit capacitor

connection points.

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2. (Original) The circuit of claim 1, wherein the turn-off semiconductor switches of each converter valve and of each cross arm are arranged side-by-side in a corresponding compression assembly in such a way that the valve connection points and the capacitor connection points are each located on a different side of the compression assembly.

Claims 3 and 4 (Canceled)

 (Original) The circuit of claim 1, wherein the turn-off semiconductor switches comprise Insulated Gate Bipolar Transistors (IGBT).

6. (Currently amended) An n-point-converter circuit, comprising: <u>a maximum of two converter valves for each converter phase electrically</u> connected in series and having free ends forming DC-side terminals, each converter valve having <u>a maximum of (n-1) turn-off semiconductor switches</u> and a valve connection point;

a voltage intermediate circuit having <u>a maximum of</u> (n-1) capacitors electrically connected in series at (n-2) corresponding capacitor connection points and connected electrically in parallel with the DC-side terminals, each capacitor connection point defining a corresponding DC potential; and <u>a maximum of</u> (n-2) cross arms having turn-off cross arm semiconductor switches and connecting one of the (n-2) capacitor connection points with a corresponding one of the valve connection points,

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connected.

wherein a number of the turn-off cross arm semiconductor switches in a cross arm is selected so that each cross arm that connects a valve connection point with a corresponding intermediate potential of the voltage intermediate circuit capacitor connection points has an identical number of turn-off cross arm semiconductor switches, and at least two of the turn-off cross arm semiconductor switches in a cross arm are anti-serially

7. (Previously presented) The circuit of claim 6, wherein the turn-off

semiconductor switches of each converter valve and the turn-off

semiconductor switches of each cross arm are arranged side-by-side in a

corresponding compression assembly in such a way that the valve

connection points and the capacitor connection points are each located on a

different side of the compression assembly.

8. (Previously presented) The circuit of claim 6, wherein the turn-off

semiconductor switches comprise Insulated Gate Bipolar Transistors

(IGBT).

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